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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/916,271	07/30/2001	Chen-Ho Lee	112.P14007	8683
43831	7590	01/12/2006	EXAMINER	
BERKELEY LAW & TECHNOLOGY GROUP 1700NW 167TH PLACE SUITE 240 BEAVERTON, OR 97006			QUIETT, CARRAMAH J	
			ART UNIT	PAPER NUMBER
			2612	

DATE MAILED: 01/12/2006

Please find below and/or attached an Office communication concerning this application or proceeding.



**DETAILED ACTION*****Response to Arguments***

1. Applicant's arguments filed 11/08/2005 have been fully considered but they are not persuasive.

For claim 1, Applicant asserts that Watanabe does not relate to staggered sensors. The Examiner respectfully disagrees. In col. 8, lines 27-42, Watanabe teaches that, "...a video signal on each pixel is read at a time in a zigzag manner from two rows of pixel groups 301a and 301b connected to one vertical drive line 2 (see read group (1) in fig. 8). Similarly, when a drive signal is applied to the next vertical drive line 2, a video signal on each pixel is read in a zigzag manner from two rows of pixel groups 301a' and 301b' connected to this vertical drive line 2 (see read group (2) in fig. 8)." As illustrated in figs. 7 and 8, the pixels are positioned in a zigzag manner. Please note that the word "zigzag" is a synonym for the word "stagger". Accordingly, claims 1-11 are rejected using the same prior art as discussed in the previous Office Action.

***Claim Objections***

2. Claim 1 is objected to because of the following informalities: The second limitation of claim one is written as, "reading out said pixel signals from said consecutive photocells of one said linear image sensor, without inserting said pixel signals *form* said other linear image sensor." This limitation should be written as, "reading out said pixel signals from said consecutive photocells of one said linear image sensor, without inserting said pixel signals *from* said other linear image sensor." Appropriate correction is required.

***Claim Rejections - 35 USC § 102***

3. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.

4. **Claims 1, 3-4, 6-7, and 9<sup>and 10</sup>** are rejected under 35 U.S.C. 102(e) as being anticipated by Watanabe (U.S. Pat. #6,522,356).

As for **claim 1**, Watanabe teaches a method of reading pixel signals from a staggered sensor, in figures 9-11, said method comprising:

providing said staggered sensor which comprises at least two linear image sensors, wherein a plurality of photocells of one said linear image sensor are offset abutting with a plurality of photocells of said adjacent linear image sensor respectively (fig. 9; col.8, line 45 - col. 9, line 21); and

reading out said pixel signals from said consecutive photocells of one said linear image sensor, without inserting said pixel signals from said other linear image sensor (fig. 11, col. 10, lines 10-63).

Regarding **claim 3**, Watanabe teaches the method of claim 1, wherein said photocells comprise a plurality of sensors of complementary metal oxide semiconductor. In col. 8, lines 45-51, Watanabe states that an X-Y scan reading type imaging apparatus is illustrated in fig. 9 with a plurality of pixels. X-Y scan reading type imaging apparatus inherently has a plurality of complementary metal oxide semiconductor (CMOS) sensors.

For **claim 4**, Watanabe further teaches the method of claim 1, wherein said reading out step is coordinated with at least a series of clock pulses (figs. 9-11; col. 9, line 22 - col. 10, line 63).

As for **claim 6**, Watanabe teaches a method of video output applicable on a multiple staggered sensor in a scanner, in figures 9-11, said method comprising:

providing at least two sensor rows in said multiple staggered sensor, each said sensor row consisting of a plurality of photocells (fig. 9; col.8, line 45 - col. 9, line 21);

reading a scan line with a plurality of pixels by one of said sensor row to generate a first consecutive video signals (fig. 11, col. 10, lines 10-63);

offsetting reading said scan line with said pixels by the other of said sensor row to generate a second consecutive video signals (fig. 11, col. 10, lines 10-63); and

outputting said video output consisting of at least said first consecutive video signals(fig. 11, col. 10, lines 10-63).

As for **claim 7**, Watanabe further teaches the method of claim 6, wherein said photocells of one said sensor row are offset abutting with said photocells of the other adjacent sensor row respectively (fig. 3A; fig. 9; col. 6, lines 50-61, col.8, lines 45-64).

Regarding **claim 9**, Watanabe further teaches the method of claim 6, wherein said photocells comprise a plurality of sensors of complementary metal oxide semiconductor. In col. 8, lines 45-51, Watanabe states that an X-Y scan reading type imaging apparatus is illustrated in fig. 9 with a plurality of pixels. X-Y scan reading type imaging apparatus inherently has a plurality of complementary metal oxide semiconductor (CMOS) sensors.

Lastly for **claim 10**, Watanabe teaches the method of claim 6 wherein said video output further comprises said second consecutive video signals (fig. 11, col. 10, lines 10-63).

***Claim Rejections - 35 USC § 103***

5. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.

6. **Claims 2, 5, 8, and 11** are rejected under 35 U.S.C. 103(a) as being unpatentable over Watanabe (U.S. Pat. #6,522,356) in view of Inuiya (U.S. Pat. #5,982,984).

As for **claim 2**, Watanabe fails to teach a method wherein said photocells comprise a plurality of charge-coupled devices (CCDs). However, he discusses a basic filter management of a CCD in the background of his disclosure (col. 1, line 36 - col. 2, line 30).

Inuiya discloses photocells comprising a plurality of charge-coupled devices (CCDs). In figure 1, Inuiya illustrates three CCDS (refs. 14R, 14G, 14B). Then, in figure 19, he illustrates the readout of the CCDS. In col. 20, lines 29-62, Inuiya discloses two horizontal transfers, which allows pixel signals to be read out from the consecutive photodiodes of the sensors, without inserting the pixel signals from the other sensor. It would have been obvious to one of ordinary skill in the art at the time the invention was made to implement Watanabe's invention with the CCDS of Inuiya in order to improve the quality of the recorded image (Inuiya, col. 1, line 20 - col. 2, line 8).

For **claim 5**, Watanabe does not teach the method of claim 1, wherein said reading out step is followed by outputting said pixel signals from said consecutive photocells of one said linear image sensor into an analog/digital converter. Inuiya, on the other hand, illustrates an analog/digital converter (ref. 16) in figure 1 via the CDS circuits (ref. 15). It would have been obvious to one of ordinary skill in the art at the time the invention was made to implement

Watanabe's invention with the A/D converter of Inuiya in order to improve the quality of the recorded image (Inuiya, col. 1, line 20 - col. 2, line 8).

For **claim 8**, the claim is very similar to the limitation in claim 2. Therefore, claim 8 is analyzed and rejected as discussed in claim 2.

For **claim 11**, the claim is very similar to the limitation in claim 5. Therefore, claim 11 is analyzed and rejected as discussed in claim 5.

### ***Conclusion***

7. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Carramah J. Quiett whose telephone number is (571) 272-7316. The examiner can normally be reached on 8:00-5:00 M-F.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, NgocYen Vu can be reached on (571) 272-7320. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

CJQ

January 9, 2006



NGOC-YEN VU  
PRIMARY EXAMINER